

Related posts:

1. 10's Complement | Prof. Jayesh Umre
2. Hexadecimal TO Octal | Prof. Jayesh Umre
3. 9's Complement | Prof. Jayesh Umre
4. Half Adder | Prof. Jayesh Umre
5. Binary to Octal Conversion | Prof. Jayesh Umre
6. Hexadecimal to Decimal | Prof. Jayesh Umre
7. Binary to Decimal | Prof. Jayesh Umre
8. 2's Complement | Prof. Jayesh Umre
9. Substraction by 2'sComplement | Prof. Jayesh Umre
10. FLip Flop | Basic Circuit diagra | Prof. Jayesh Umre
11. K-MAP SOP 4 variables | Prof. Jayesh Umre
12. Binary Addition 4bit 8bit GATE UGC NET | Prof. Jayesh Umre
13. Binary Substraction | GATE UGC NET | Prof. Jayesh Umre
14. Binary Division | GATE UGC NET| Prof. Jayesh Umre
15. Octal to Hexadecimal | | Prof. Jayesh Umre
16. Decimal to Octal Conversion| Prof. Jayesh Umre
17. Full Adder | Block diagram | Truth table | Circuit diagram | Prof. Jayes...
18. Binary Multiplication | GATE UGC NET | Prof. Jayesh
19. K-MAP POS form 3 variables | GATE UGC NET | Prof. Jayesh Umre
20. 4:1 Multiplexer| Block and circuit diagram | function table | Prof. Jaye...
21. Multiplexer | N:1 | 2:1| Block & Circuit diagram | Function table | Prof...
22. Demultiplexer | 1:N | 1:2 | Prof. Jayesh Umre
23. Explain De-Morgan's theorem with suitable example.