#1. What does GPU stand for in computer graphics technology?

☐ 1. Graphics Processing Unit

1. Graphics Processing Unit

☐ 2. General Processing Unit

2. General Processing Unit

☐ 3. Graphical Performance Unit

3. Graphical Performance Unit

☐ 4. General Purpose Unit

4. General Purpose Unit

☐ 5. Graphical Processing Option

5. Graphical Processing Option

#2. Which of the following components is responsible for managing the system clock in a computer system?

☐ 1. CPU

1. CPU

☐ 2. GPU

2. GPU

☐ 3. RAM

3. RAM

☐ 4. CMOS Battery

4. CMOS Battery

☐ 5. Hard Disk

5. Hard Disk

#3. What is the purpose of an instruction register in a CPU?

☐ 1. To store the data to be processed

1. To store the data to be processed

☐ 2. To store the current instruction

2. To store the current instruction

☐ 3. To store the result of an operation

3. To store the result of an operation

☐ 4. To store the address of data

4. To store the address of data

☐ 5. To store the BIOS instructions

5. To store the BIOS instructions

#4. In computer architecture, what does the term "word" refer to?

☐ 1. A single character

1. A single character

☐ 2. A group of bits representing a data item

2. A group of bits representing a data item

☐ 3. A storage unit in the hard disk

3. A storage unit in the hard disk

☐ 4. A unit of memory in a CPU

4. A unit of memory in a CPU

☐ 5. A file or document in the computer

5. A file or document in the computer

#5. Which of the following is a characteristic of a RISC (Reduced Instruction Set Computing) architecture?

☐ 1. Large number of instructions

1. Large number of instructions

☐ 2. Simple instructions

2. Simple instructions

☐ 3. Complex instructions

3. Complex instructions

☐ 4. Slow execution speed

4. Slow execution speed

☐ 5. None of the above

5. None of the above

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#6. What is the role of the arithmetic logic unit (ALU) in a CPU?

☐ 1. To perform arithmetic and logic operations

1. To perform arithmetic and logic operations

☐ 2. To manage memory operations

2. To manage memory operations

☐ 3. To control input/output operations

3. To control input/output operations

☐ 4. To decode instructions

4. To decode instructions

☐ 5. None of the above

5. None of the above

#7. What is the purpose of pipelining in computer architecture?

☐ 1. To transmit data between devices

1. To transmit data between devices

☐ 2. To execute multiple instructions simultaneously

2. To execute multiple instructions simultaneously

☐ 3. To store temporary data

3. To store temporary data

☐ 4. To enhance instruction processing speed

4. To enhance instruction processing speed

☐ 5. To manage input/output operations

5. To manage input/output operations

#8. What does the term "clock cycle" refer to in a CPU?

☐ 1. The time taken to execute one instruction

1. The time taken to execute one instruction

☐ 2. The time taken to perform one clock pulse

2. The time taken to perform one clock pulse

☐ 3. The time taken to switch off the CPU

3. The time taken to switch off the CPU

☐ 4. The time taken to restart the CPU

4. The time taken to restart the CPU

☐ 5. None of the above

5. None of the above

#9. In computer architecture, what is the purpose of the address bus?

☐ 1. To carry instructions to the CPU

1. To carry instructions to the CPU

☐ 2. To carry data between CPU and memory

2. To carry data between CPU and memory

☐ 3. To carry memory addresses

3. To carry memory addresses

☐ 4. To carry instructions from the CPU

4. To carry instructions from the CPU

☐ 5. None of the above

5. None of the above

#10. What is the role of the control bus in a computer system?

☐ 1. To control the input devices

1. To control the input devices

☐ 2. To manage the main memory

2. To manage the main memory

☐ 3. To control communication between components

3. To control communication between components

☐ 4. To control the output devices

4. To control the output devices

☐ 5. None of the above

5. None of the above

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#11. What is the function of the system bus in a computer system?

☐ 1. To connect the CPU to external devices
1. To connect the CPU to external devices
☐ 2. To connect the CPU to the motherboard
2. To connect the CPU to the motherboard
☐ 3. To connect the CPU to the memory
3. To connect the CPU to the memory
☐ 4. To connect the CPU to the hard disk
4. To connect the CPU to the hard disk
☐ 5. To connect the CPU to the power supply
5. To connect the CPU to the power supply

#12. What does the term "DMA" stand for in computer architecture?

☐ 1. Direct Memory Access
1. Direct Memory Access
☐ 2. Dynamic Memory Allocation

2. Dynamic Memory Allocation

☐ 3. Digital Media Adapter

3. Digital Media Adapter

☐ 4. Dual Mode Access

4. Dual Mode Access

☐ 5. Data Management Architecture

5. Data Management Architecture

#13. What is the purpose of a memory controller in a computer system?

☐ 1. To manage the main memory

1. To manage the main memory

☐ 2. To control input devices

2. To control input devices

☐ 3. To manage cache memory

3. To manage cache memory

☐ 4. To control output devices

4. To control output devices

☐ 5. None of the above

5. None of the above

#14. What does the term "cache hit" refer to in computer architecture?

☐ 1. A cache memory error

1. A cache memory error

☐ 2. A cache memory access operation that finds data

2. A cache memory access operation that finds data

☐ 3. A cache memory access operation that misses data

3. A cache memory access operation that misses data

☐ 4. A cache memory access operation that fails

4. A cache memory access operation that fails

☐ 5. None of the above

5. None of the above

#15. Which of the following is a characteristic of a cache memory?

☐ 1. Small in size and high in speed

1. Small in size and high in speed

☐ 2. Large in size and low in speed

2. Large in size and low in speed

☐ 3. Small in size and low in speed

3. Small in size and low in speed

☐ 4. Large in size and high in speed

4. Large in size and high in speed

☐ 5. None of the above

5. None of the above

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#16. What does the term "virtual memory" refer to in computer systems?

☐ 1. Physical memory allocated for virtual tasks
1. Physical memory allocated for virtual tasks
☐ 2. Memory reserved for operating system use
2. Memory reserved for operating system use
☐ 3. An imaginary memory space
3. An imaginary memory space
☐ 4. RAM used for storing virtual objects
4. RAM used for storing virtual objects
☐ 5. None of the above
5. None of the above

#17. What is the purpose of a memory hierarchy in computer architecture?

☐ 1. To confuse computer users
1. To confuse computer users
☐ 2. To slow down data processing
2. To slow down data processing
☐ 3. To manage different types of memory
3. To manage different types of memory
☐ 4. To reduce memory access time
4. To reduce memory access time
☐ 5. None of the above
5. None of the above

#18. What does the term "bus width" refer to in computer systems?

☐ 1. The length of a bus
1. The length of a bus

☐ 2. The speed of a bus

2. The speed of a bus

☐ 3. The number of bits that can be transferred in parallel

3. The number of bits that can be transferred in parallel

☐ 4. The capacity of a bus

4. The capacity of a bus

☐ 5. None of the above

5. None of the above

#19. In computer architecture, what does the term "word size" refer to?

☐ 1. The size of a computer program

1. The size of a computer program

☐ 2. The size of a memory address

2. The size of a memory address

☐ 3. The size of a data bus

3. The size of a data bus

☐ 4. The size of a CPU register

4. The size of a CPU register

☐ 5. The size of a computer screen

5. The size of a computer screen

#20. What is the purpose of the arithmetic logic unit (ALU) in a CPU?

☐ 1. To perform arithmetic and logic operations

1. To perform arithmetic and logic operations

☐ 2. To manage memory operations

2. To manage memory operations

☐ 3. To control input/output operations

3. To control input/output operations

☐ 4. To decode instructions

4. To decode instructions

☐ 5. None of the above

5. None of the above

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4. Cryptography MCQs

5. Computer Networks MCQs
6. Machine Learning MCQs
7. OPERATING SYSTEMS MCQ
8. Programming Practices MCQ
9. Ecosystems MCQ
10. Biodiversity and its conservation MCQ
11. Environmental Pollution mcq
12. Graph Theory and Combinatorics MCQ
13. Relational algebra,Functions and graph theory MCQ
14. Data Structure MCQ
15. Combinational Logic MCQ
16. Sequential logic MCQ
17. Analog/Digital Conversion, Logic Gates, Multivibrators, and IC 555 MCQ
18. Polymorphism MCQ
19. Library Management System MCQ
20. Numerical Methods MCQ
21. Algorithmic Problem MCQ
22. Trees, Graphs, and NP-Completeness MCQ
23. The Software Product and Software Process MCQ
24. Computer Arithmetic MCQ
25. I/O Organization MCQ
26. Memory Organization MCQ
27. Input / Output MCQ
28. Operating Systems and Concurrency
29. Software Development and Architecture MCQ
30. Fuzzy Systems MCQ
31. Genetic Algorithms MCQ

32. Rough Set Theory MCQ
33. RL Techniques MCQs
34. Review of traditional networks MCQ
35. Study of traditional routing and transport MCQ
36. NoSQL MCQs Concepts, Variations, and MongoDB
37. Mining social Network Graphs MCQ
38. Mathematical Background for Cryptography MCQ
39. OLAP Systems MCQ
40. Introduction to Data& Data Mining MCQ
41. Supervised Learning MCQ
42. Agile Software Design and Development MCQs
43. Machine Learning Fundamentals MCQs
44. Neural Network MCQs
45. MAC Sub layer MCQ
46. Network Layer MCQ
47. Transport Layer MCQ
48. Raster Scan Displays MCQs
49. 3-D Transformations MCQs
50. Code Generation MCQs
51. Code Optimization MCQs
52. INTRODUCTION Knowledge Management MCQs
53. Software Management Process MCQs
54. Software Management Disciplines MCQs
55. Rural Management MCQs
56. Sensors and Actuators MCQs
57. IoT MCQs: Basics, Components, Protocols, and Applications
58. MCQs on IoT Protocols

59. Block chain application development MCQs
60. MCQs on Service Oriented Architecture, Web Services, and Cloud Computing
61. Parallel Computing MCQs
62. Efficient Open MP Programming MCQs
63. CVIP Basics MCQs
64. Image Representation and Description MCQs
65. IoT Essentials MCQs
66. Sensor and Actuator MCQs
67. Innovation in Business: MCQs
68. Automata Theory MCQs
69. Data Base Design MCQs
70. Transaction Processing Concepts MCQs
71. HADOOP MAPREDUCE MCQs
72. BIG DATA TOOLS AND TECHNIQUES MCQs
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74. Cyber Crime and Criminal justice MCQs
75. Basics of programming MCQs
76. Decision control structure MCQs
77. Introduction to Energy Science MCQs
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80. State-Space Analysis, Sampling Theorem, and Signal Reconstruction mcqs
81. Frequency Domain Analysis MCQs
82. System Design and Compensation Techniques MCQs
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84. Discrete-Time Signals and Systems MCqs
85. Types of antennas mcqs

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96. Coding theorem MCQs
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100. IoT Technologies MCQs
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102. Optical Fiber Basics MCQs
103. 5G Transmission and Design Techniques MCQS
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112. Introduction to circuit theory MCQS

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120. Architectural Principles MCQs
121. Review of Fluid Properties MCQs
122. Kinematics of Flow MCQs
123. Airport Planning and Geometrical Elements MCQs
124. Airport, Obstructions, Lightning & Traffic control MCQs
125. Rate Analysis MCQs
126. Detailed Estimates MCQs
127. Docks and Locks MCQS
128. Urban Planning MCQs
129. Geographic Information System MCQS
130. Data Models mCQs
131. Entrepreneurship MCQs
132. Motivation MCQS
133. Floor and Roof Construction MCQs
134. Earthquake-Resistant Building MCQs
135. Railway Track Design and Signaling MCQs
136. Bridge Construction Essentials MCQs
137. Geology, Remote Sensing, and GIS MCQs
138. Waste water Treatment Operations MCQs
139. Response to harmonic and periodic vibrations MCQS

140. Response to Arbitrary, Step, and Pulse Excitation MCQS
141. Miscellaneous Services MCQS
142. Basic Principles of Structural Design MCQs
143. Water Treatment methods MCQs
144. Sewerage Systems MCQS
145. Prefabrication in Construction MCQs
146. Prefabricated Construction MCQs
147. Rigid pavement design MCQs
148. Evaluation and Strengthening of Existing Pavements MCQS
149. Uniform flow in open channels MCQs
150. Non uniform flow in open channels MCQs
151. Contemporary Issues & Enforcement of IPR MCQs
152. Concept of EIA MCQs
153. Inventory Models MCQs
154. Queueing Models MCQS
155. Decision Models MCQs
156. Hydrological Cycle mCQs
157. Hydrological Measurement MCQs
158. Groundwater and Well Dynamics MCQs
159. Foundations on problematic soil & Introduction to Geosynthetics MCQs
160. Retaining Walls and Earth Pressure MCQs
161. Types of Bridge Super Structures MCQs
162. Response Spectrum MCQs
163. Aseismic Structural Modelling MCQS
164. Design of structure for earthquake resistance MCQS
165. Introduction to learning ,ANN MCQs
166. Concrete Structure MCQs

167. Damage Assessment MCQs
168. Integrated Water Resources Management (IWRM) Approach MCQs
169. Surface and Subsurface Water Systems MCQS
170. Conventional and Non-conventional Techniques for Water Security MCQs
171. Fundamental Aspects of Vibrations MCQs
172. Damped Free Vibrations: Viscous damping MCQs
173. Harmonically excited Vibration MCQS
174. Electrical and Hydraulic Actuators MCQs
175. SINGLE CONDITIONING MCQs
176. Dynamics of Engine Mechanisms MCQs
177. Liquid alternative fuels MCQs
178. Gaseous Fuels MCQs
179. Automobile emissions MCQS
180. Display systems and anthropometric datA MCQs
181. Quality Management MCQs
182. Quality Management process MCQs
183. Assembly of Elements and Matrices MCQs
184. Higher Order and Isoparametric Elements MCQs
185. Static & Dynamic Analysis MCQs
186. Chassis & Body Engg MCQs
187. Steering System MCQs
188. Transmission System MCQs
189. Understanding Wear Mechanisms MCQs
190. Lubricants and Lubrication Standards MCQS
191. Nano Tribology MCQs
192. Production Systems MCQs
193. Work Study MCQs

194. Production Planning MCQs
195. Energy Management MCQs
196. Energy Audit MCQs
197. Material energy balance MCQs
198. Productivity and Operations MCQs
199. Entrepreneurship MCQs
200. Introduction of MIS MCQs