1. Which peripheral device is responsible for generating precise time intervals in a computer system?

a) 8253
b) 8254
c) 8259A
d) 8257

Answer: b) 8254

Explanation: The 8254 programmable interval timer is specifically designed for generating precise time intervals in a computer system. It is commonly used for tasks such as generating system clock ticks and setting timeouts.

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2. Which component is crucial for managing interrupt requests in a computer system?

a) 8253
b) 8254
c) 8259A
d) 8257

Answer: c) 8259A

Explanation: The 8259A programmable interrupt controller is essential for managing interrupt requests from various peripheral devices in a computer system. It prioritizes and manages

these requests to ensure proper handling by the CPU.

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3. Which peripheral device facilitates direct memory access (DMA) operations in a computer system?

a) 8253
b) 8254
c) 8259A
d) 8257

Answer: d) 8257

Explanation: The 8257 DMA controller enables direct memory access operations, allowing certain peripheral devices to transfer data to and from memory without CPU intervention. This enhances system efficiency by offloading data transfer tasks from the CPU.

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4. Which component is responsible for serial input/output (I/O) and data communication in a computer system?

a) 8253
b) 8254
c) USART

d) 8257

Answer: c) USART

Explanation: USART (Universal Synchronous Asynchronous Receiver Transmitter) is responsible for serial input/output and data communication in a computer system. It facilitates the exchange of data between the computer and external devices using serial communication protocols.

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5. Which peripheral device is primarily used for generating clock signals in a computer system?

a) 8253
b) 8254
c) 8259A
d) USART

Answer: a) 8253

Explanation: The 8253 programmable interval timer is commonly used for generating clock signals in a computer system. It can be programmed to generate various timing and clock signals required by the system.

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6. Which component handles priority and masking of interrupt requests in a computer system?

a) 8253
b) 8254
c) 8259A
d) USART

Answer: c) 8259A

Explanation: The 8259A programmable interrupt controller handles the priority and masking of interrupt requests in a computer system. It allows the system to manage multiple interrupt sources and prioritize their handling by the CPU.

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7. Which peripheral device is crucial for controlling the flow of data between memory and I/O devices without CPU intervention?

a) 8253
b) 8254
c) 8259A
d) 8257

Answer: d) 8257

Explanation: The 8257 DMA controller is essential for controlling the flow of data between memory and I/O devices without CPU intervention. It enhances system performance by allowing data transfer operations to occur independently of the CPU.

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8. Which component is essential for asynchronous communication between computer systems?

a) 8253
b) 8254
c) USART
d) 8257

Answer: c) USART

Explanation: USART (Universal Synchronous Asynchronous Receiver Transmitter) is crucial for asynchronous communication between computer systems. It supports both synchronous and asynchronous data transmission, making it versatile for various communication needs.

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9. Which peripheral device is commonly used for generating system clock ticks in a computer system?

a) 8253
b) 8254
c) 8259A
d) USART

Answer: b) 8254

Explanation: The 8254 programmable interval timer is commonly used for generating system clock ticks in a computer system. It provides precise timing signals that are essential for system synchronization and operation.

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10. Which component facilitates the prioritization of interrupt requests in a computer system?

a) 8253
b) 8254
c) 8259A
d) USART

Answer: c) 8259A

Explanation: The 8259A programmable interrupt controller facilitates the prioritization of interrupt requests in a computer system. It ensures that higher-priority interrupts are serviced first, allowing for efficient handling of system events.

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