What is the format of Micro Instruction in Computer Architecture explain?

Write a short note on LRU algorithm?

Write a short note on Array processors?

Write a short note on design of arithmetic unit?

What is the layout of pipelined instruction in Computer Architecture?

Explain the following interfaces in Detail:PCI Bus, SCSI Bus, USB Bus

What is Memory Organization ? Discuss different types of Memory Organization in Computer System.

Briefly explain the concept of pipelining in detail?

What is Multiprocessor? Explain inter process communication in detail?

Write short note on improving cache performance methods in detail?

Briefly explain the concept of pipelining in detail?

What is Multiprocessor? Explain inter process communication in detail?

Explain different modes of data transfer between the central computer and I/O device?

Explain how addition and subtraction are performed in fixed point number?

Explain the design of arithmetic and logic unit by taking on example?

Define the instruction format? Explain I/O System in detail?

Discuss the following in detail: RISC architecture, Vector processing?

Explain how a stack organized computer executes instructions? What is Stack?

Explain hardwired microprogrammed control unit? What is address sequencer circuit?

If cache access time is IOOns, main memory access time is 1000 ns and the hit ratio is 0.9.

Find the average access time and also define hit ratio.

Explain signed magnitude, signed I's complement and signed 2's complement representation of numbers. Find the range of numbers in all three representations for 8 bit register.

Differentiate between Serial and parallel data transfer?

What is Paging? Explain how paging can be implemented in CPU to access virtual memory.

What is Associative memory? Explain the concept of address space and memory space in

Virtual memory.

Draw and explain the memory hierarchy in a digital computer. What are advantages of cache memory over main memory?

Explain general register organization.

Draw the functional and structural views of a computer system and explain in detail?

Write short notes on

Explain SIMD array processor along with its architectural diagram?

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