Write short note on improving cache performance methods in detail ?

Caches are an essential component of modern computer architectures that help to reduce the latency of accessing data from main memory.

Some methods for improving cache performance:

1. Increasing Cache Size
2. Increasing Cache Associativity
3. Using Multilevel Caches
4. Cache Block Size Optimization
5. Using Cache Prefetching
6. Using Cache Replacement Policies

1. Increasing Cache Size:

Increasing cache size is a straightforward way to improve cache performance. A larger cache can hold more data, reducing the number of cache misses and thereby improving cache hit rate. However, increasing the cache size may also increase the cache access time and power consumption.

2. Increasing Cache Associativity:

Cache associativity is the number of cache lines that can map to a particular cache set. Higher associativity means more cache lines can map to the same cache set, reducing the number of cache conflicts and improving cache hit rate. However, higher associativity also increases cache access time and power consumption.

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3. Using Multilevel Caches:

Multilevel caches can help improve cache performance by using multiple levels of cache. A small, fast, and expensive cache can be used as the first level cache, while a larger, slower, and cheaper cache can be used as the second level cache. This configuration can help to reduce the average access time while minimizing cost and power consumption.

4. Cache Block Size Optimization:

The size of the cache block can have a significant impact on cache performance. A smaller block size can help to reduce cache conflicts, while a larger block size can help to increase cache hit rate. However, larger block sizes also increase the likelihood of cache pollution and may increase cache access time.

5. Using Cache Prefetching:

Cache prefetching can help to improve cache performance by anticipating data access patterns and loading data into the cache before it is needed. Prefetching can be performed either explicitly, by the programmer, or implicitly, by the hardware.

6. Using Cache Replacement Policies:

Cache replacement policies determine which cache block is evicted when the cache is full. Different replacement policies can have a significant impact on cache performance, depending on the access patterns. Common policies include Least Recently Used (LRU), First-In-First-Out (FIFO), and Random Replacement.

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26. Computer Organization Q and A

27. What is Multiprocessor ? Explain inter process communication in detail ?

28. Briefly explain the concept of pipelining in detail ?

29. Discuss the following in detail: RISC architecture, Vector processing ?

30. Define the instruction format ? Explain I/O System in detail ?

31. Explain the design of arithmetic and logic unit by taking on example ?

32. Explain how addition and subtraction are performed in fixed point number ?

33. Explain different modes of data transfer between the central computer and I/O device ?

34. Differentiate between Serial and parallel data transfer ?

35. Explain signed magnitude, signed l's complement and signed 2's complement representation of numbers. Find the range of numbers in all three representations for 8 bit register.

36. If cache access time is lOOns, main memory access time is 1000 ns and the hit ratio is 0.9. Find the average access time and also define hit ratio.

37. Explain hardwired microprogrammed control unit ? What is address sequencer circuit ?

38. Explain how a stack organized computer executes instructions? What is Stack?

39. Draw and explain the memory hierarchy in a digital computer. What are advantages of cache memory over main memory?

40. What is Associative memory? Explain the concept of address space and memory space in Virtual memory.

41. What is Paging? Explain how paging can be implemented in CPU to access virtual memory.

42. Explain SIMD array processor along with its architectural diagram ?

43. Write short notes on

44. Draw the functional and structural views of a computer system and explain in detail ?

45. Explain general register organization.

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46. Compare and contrast DMA and I/O processors ?

47. Define the following: a) Flynn's taxonomy b) Replacement algorithm

48. Explain the various pipeline vector processing methods ?

49. Describe the language features for parallelism ?

50. What are different addressing modes? Explain them.

51. Explain any page replacement algorithm with the help of example ?

52. What is mapping? Name all the types of cache mapping and explain anyone in detail.

53. Explain arithmetic pipeline ?

54. Write short notes on, a) SIMD, b) Matrix multiplication c) Instruction format

55. Differentiate: a) Maskable and non-maskable interrupt b) RISC and CISC

56. Computer Organization Previous Years Solved Questions

57. Booths algorithm to muliyiply +5 and -15